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... passed 1bma^{12A} 78 to duce me **Register** 70 simply ... control signal tions through the **B Bypass Mux** 53 and ... Also, decoder 110 is **disabled** so that only the leading ...

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[High speed parallel binary multiplier](#)

JW Kronlage - US Patent 4,809,211, 1989 - Google Patents

... There is also a **bypass** line 39 coupled from line 38 ... CKEY on line 37 allows loading of the Y **register** 36 when ... The output of the Y **multiplexer** 54 for DA and DB ...

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[PDF] [Power reduction in VLIW processor with compiler driven bypass network](#)

N Goel, A Kumar, PR Panda - VLSID - Citeseer

... number of in- puts to **bypass multiplexer** will increase ... com- piler's hints hardware for **bypass** control is ... allocation to fully exploit the **by-pass** architecture. ...

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[Boundary-scan register cell with bypass circuit](#)

NG Jacobson, DR Curd - US Patent 6,314,539, 2001 - Google Patents

... pass data signals output from the shift **register** to the ... from the BSR chain) when the **bypass MUX** is controlled ... BSR cell also includes mode control **MUX** having a ...

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WL Saltz, AS Vezza - US Patent 4,357,656, 1982 - Google Patents

... perfor- mer had the ability to **disable** or "force ... bit location of the cache control **register** 96 are ... an individual output of the index **multiplexer** 46 containing ...

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[... a microinstruction with operands specified by an instruction held in an alias register](#)

A Carbine, FS Smith - US Patent 5,222,244, 1993 - Google Patents

... 99 LO6IC MICRO-FLOW I12— WORD **MULTIPLEXER** CONSTANT BUS ... 1 ^ 8T J J L **BYPASS** **MULTI- PLEXER** ... CONTENTS AVAILABLE TO THE MOUSETRP **MUX**,THROUGHTHE TRANSLATION ...

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... no optimization techniques are applied to remove or prevent ... slight increase in the overall **register** file area ... in control logic complexity and **bypass mux** size. ...

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... control this network, over 280 **by- pass** comparators are ... register must take precedence over **register file** data as ... are all kept locally in **bypass** control latches ...Cited by 53 - [Related articles](#) - [View as HTML](#) - [BL Direct](#) - [All 27 versions](#)[A novel renaming scheme to exploit value temporal locality through physical register reuse ...- ► psu.edu](#)[\[PDF\]](#)

S Jourdan, R Ronen, M Bekerman, B Shomar, ... - MICRO-ANNUAL WORKSHOP THEN ANNUAL ..., 1998 -

[doi.ieeeecomputersociety.org](#)... scheme introduced in [Mosh97b] proposed to **bypass** the memory ... no writing is performed into the **register file** on a ... This gives the opportunity to remove all the ...Cited by 100 - [Related articles](#) - [BL Direct](#) - [All 26 versions](#)[Register packing: Exploiting narrow-width operands for reducing **register file** pressure- ► psu.edu \[pdf\]](#)

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... The base case **register file** always accesses both operands even if the machine ... all instructions make use of all the values in the **by- pass** latches ... 7. **Bypass** Skip ...Cited by 52 - [Related articles](#) - [View as HTML](#) - [All 32 versions](#)[The PowerPC 604 risc microprocessor- ► gatech.edu \[pdf\]](#)SP Song, M Denman, J Chang - IEEE Micro, 1994 - [doi.ieeeecomputersociety.org](#)... **By- pass** logic permits most instructions to complete and write back ... operand to be written to the **register file**, even if ... is a miss, the unit will **bypass** it in ...Cited by 124 - [Related articles](#) - [BL Direct](#) - [All 7 versions](#)[\[PDF\] ► Itanium 2 processor microarchitecture](#)C McNairy, D Soltis - IEEE Micro, 2003 - [dei.polimi.it](#)... These capabilities remove the burden of ordering and padding instructions ... and eight write ports on the integer **register file** and 20 **bypass** choices support ...Cited by 123 - [Related articles](#) - [View as HTML](#) - [BL Direct](#) - [All 42 versions](#)[\[PDF\] ► A 160-mhz, 32-b, 0.5-w CMOS RISC microprocessor](#)

J Montanaro, RT Witek, K Anne, AJ Black, ... - IEEE Journal of Solid-State Circuits, 1996 - Citeseer

... at the end of cycle 3, we **bypass** the **result** ... so for this case, the shifter is **disabled** and bypassed to ... a two-cycle latency for return data to the **register file**. ...Cited by 543 - [Related articles](#) - [View as HTML](#) - [BL Direct](#) - [All 11 versions](#)[\[PDF\] ► Dual-core execution: Building a highly scalable single-thread instruction window](#)

H Zhou - Proceedings of the 14th International Conference on ..., 2005 - Citeseer

... keeping the decoded/renamed version can **bypass** some of ... to the front processor's physical **register file**, the renaming ... mode, such invalidation is **disabled** at the ...Cited by 54 - [Related articles](#) - [View as HTML](#) - [All 21 versions](#)[Boosting beyond static scheduling in a superscalar processor- ► psu.edu \[pdf\]](#)

MD Smith, MS Lam, MA Horowitz - Proceedings of the 17th annual international ..., 1990 - portal.acm.org

... More recent VLIW machines [5] remove this restriction ... allowing loads and **stores** to **bypass** each other ... a reorder buffer [19] associated with each **register file**. ...

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JH Tseng, K Asanovic... - ACM SIGARCH Computer Architecture News, 2003 - doi.ieeecomputersociety.org

... a microarchitecture might not support **by-pass** from every ... Wakeup Select Arbitrate

Execute Writeback **Bypass** Read ... Our **register file** structure only allows sharing ...

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... minimal extra control logic we can also **disable** word lines ... use of all the values in the **by-pass** latches. ... provide a separate zero input to the **bypass mux** we can ...[Cited by 52](#) - [Related articles](#) - [View as HTML](#) - [All 32 versions](#)[Fast floating point result alignment apparatus](#)

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GT B1Д1Д - uhra.herts.ac.uk

... HARP uses register bypassing to remove the potential ... to another pipeline »»; Compute **result** Figure 4 ... **bypass** source ШШШШ **Bypass MUX bypass** sources **Bypass** ...[Related articles](#)[Data memory and method of reading a data memory](#)

HE Oldham - US Patent 5,280,449, 1994 - Google Patents

... efficient embodi- serving as a **bypass multiplexer** is arranged ... output from the data word selection **multiplexer** 6 have ... 5 and delay line 7 is to **disable** the sense ...[Cited by 8](#) - [Related articles](#) - [All 2 versions](#)[Pipelined instruction processor with data bypassing](#)

B Srinivasan, R Sethuraman, CAA Pinto - US Patent App. 10/549,368, 2004 - Google Patents

... unit 30 is enabled when the **disable** information indicates ... 32 should be used as operand, **bypass** control unit ... from operand selection unit 34 via **multiplexer** 123. ...[All 4 versions](#)[Combination of forwarding/bypass network with history file](#)

BK Flachs, BW Michael - US Patent App. 11/095,908, 2005 - Google Patents

... 5, 2006 COMBINATION OF FORWARDING/BYPASS NETWORK WITH ... and 118 have a **multiplexer** ("MUX") 150 connected on ... **store** data **results**; a multiplexor ("MUX"), coupled to ...[All 2 versions](#)[Digital data processing apparatus having multi-level register file](#)

NS Nunamaker, JC Randolph, K Tsuchiya - US Patent App. 11/835,519, 2007 - Google Patents

... 3 406 Data 305A 34x130 bit 305B 34x130 bit 404 To **By-Pass Mux** op_conflict data ... x4)-y 3Q6A 128x128 bit 306B 128 x 128 bit To **Result Mux** To **Store** Staging To ...

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Method and apparatus for **disabling** and diagnosing cache memory storage locations

WL Saltz, AS Vezza - US Patent 4,357,656, 1982 - Google Patents

... **disabled** while allowing the remainder of cache to be Part of the ... performer had the ability to **disable** or "force ... with the output of the tag **multiplexer** 48 and ...

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I Park, MD Powell, TN Vijaykumar - MICRO-ANNUAL WORKSHOP THEN ANNUAL ..., 2002 - doi.ieeecomputersociety.org

... **bypass** inputs (a) Conventional **register file**: Each read port wired to 1 func.

unit. (b) **Bypass** hint **register file**: **mux mux** Func. Unit **mux mux** ...

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JE Smith, AR Pleszkun - IEEE Transactions on Computers, 1988 - doi.ieeecomputersociety.org

... in the reorder buffer, then a **multiplexer** is set ... are used prior to being written into the **register file**. ... an operand designator should generate a **bypass** path to ...

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... at the end of cycle 3, we **bypass** the **result** ... operations comes from a 31-entry **register file** with three ... tion code computation to swing the PC **multiplexer** for the ...

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S Agarwala, T Anderson, A Hill, MD Ales, R ... - IEEE Journal of Solid-State Circuits, 2002 - Citeseer

... to calculate controls for the RF **by-pass** logic and ... reads that are not necessary due to RF **bypass**. ... from the functional unit and the **register file** is bypassed ...

Cited by 60 - [Related articles](#) - [View as HTML](#) - [BL Direct](#) - All 7 versions

[Method and apparatus for delaying writing back the **results** of instructions to a processor](#)

PG Patel - US Patent 5,222,240, 1993 - Google Patents

... destination address, **bypass** path 200 **register file** having a ... **Bypass** path 200 connects the tempo- file comprising at ... register TEMP 148 through **multiplexer** 4 136 ...

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... 2:1 MUX 3:1 MUX 4:1 MUX 4:1 MUX ... assigned slot address are driven over a shared **bypass** bus, the ... of the register, this logic disables the **register file** write in ...

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Computer processor having a **register file** with reduced read and/or write port bandwidth

R Yung, N Wilhelm - US Patent 5,761,475, 1998 - Google Patents

... Scoreboard unit 22, and a **register file** 28 positioned ... processor 50 also includes a **bypass** logic circuit 52 ... number of source register **multiplexor (MUX)** circuit 54 ...

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